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U.S. **UTILITY** Patent Application

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Shinji Tadaki Noriyuki Awaji Fumihiro Namiki Katsuya Irie

Plasma display panel and process for manufacturing its substrate structure

ISSUING CLASSIFICATION								
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TERMINAL	DRAWINGS			CLAIMS ALLOWED		
L-J DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G	
The term of this patent (date)		,		NOTICE OF ALLOWANCE MAILE		
has been disclaimed.	(Assistant	Examiner)	(Date)			
The term of this patent shall not extend beyond the expiration date			, -			
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he information disclosed herein may be resi ossession outside the U.S. Patent & Tradem	tricted. Unauthorized nark Office is restricted	disclosure may be p to authorized emple	prohibited by the U oyees and contract	nited States Code Title 3 ors only.	5, Sections 122, 181 and 36	
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